

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,057,957 B2  
APPLICATION NO. : 10/810966  
DATED : March 26, 2004  
INVENTOR(S) : Chin-Huang Wang

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, line 36, delete "inputs;" and insert therefore -- inputs: --.  
Column 5, line 39, delete "outputs;" and insert therefore -- outputs: --.  
Column 5, line 42, delete "inputs;" and insert therefore -- inputs: --.  
Column 5, line 48, delete "inputs;" and insert therefore -- inputs: --.  
Column 6, line 13, delete "gale" and insert therefore -- gate --.  
Column 6, line 23, delete "as" and insert therefore -- is --.  
Column 7, line 10, delete "mud" and insert therefore -- and --.  
Column 7, line 16, delete "inputs;" and insert therefore -- inputs: --.  
Column 7, line 20, delete "outputs;" and insert therefore -- outputs: --.  
Column 7, line 24, delete "inputs;" and insert therefore -- inputs: --.  
Column 7, line 28, delete "output;" and insert therefore -- output: --.  
Column 7, line 31, delete "inputs;" and insert therefore -- inputs: --.  
Column 7, line 36, delete "spend" and insert therefore -- speed --.  
Column 7, line 43, delete "us" and insert therefore -- its --.  
Column 7, line 50, delete "anode" and insert therefore -- a node --.

Signed and Sealed this

Tenth Day of July, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*